



End-Semester Exam Schedule (December 2023)

**B. Tech. I – Electronics & Communication Engineering
(1st Semester)**

Exam Date	Time (09:30 AM to 12:30 PM)
18/12/2023	Mathematics-I (MA117)
19/12/2023	Semiconductor Physics and Devices (EC101)
20/12/2023	Fundamentals of Electrical Engineering (EE110)
21/12/2023	Fundamentals of Computer & Programming (CS110)
22/12/2023	English and Professional Communication (HS110)

**M. Tech. I (CS & VLSI) / Ph. D
(1st Semester)**

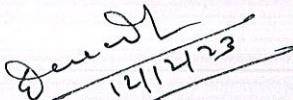
Exam Date	Time (02:00 PM to 05:00 PM)
18/12/2023	RF Circuit & System (EC601)
19/12/2023	Advance Optical Communication Systems (EC603) Hardware Description Language (EC623)
20/12/2023	Digital VLSI Design (EC609) Information Theory & Coding (EC619)
21/12/2023	Embedded System (EC613) Digital Satellite Communication (EC621)
22/12/2023	VLSI Technology (EC617)
23/12/2023	Advance Digital Signal Processing (EC605) MOS Devices & Technology (EC611)

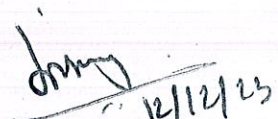
Note:

- All the students have to compulsorily carry their Hall Tickets during the examination.
- All the students are required to take their seat before 15 minutes of schedule time.

Seating Plan:

B. Tech – I	EC022N U23EC001-U23EC070 (70)	EC106N U23EC071-U23EC140 (70)	EC203N U23EC141-U23EC200 (60)
M. Tech – I & Ph.D.	EC203N: All Students		


12/12/23
Dr. Deepak Joshi Dr. Kamal Captain
Examination Coordinators, DoECE


12/12/23
HOD, DoECE

Copy to:

- 1) Dy. Registrar (Acad)
- 2) Chief Exam Coordinator
- 3) Student NB DoECE
- 4) upload@svnit.ac.in
- 5) All concerned Departments

In case any query, contact Examination Coordinators, DoECE.